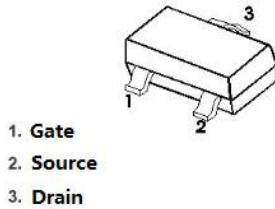


SOT-23N沟道30V漏-源电压MOS管  
N-Channel 30V(D-S) Mosfet

产品特性总结Product Summary	
VDS	30V
RDS(on)(@VGS= 10V)	<30mΩ
RDS(on)(@VGS= 4.5V)	<45mΩ

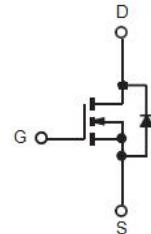
根据客户要求打印 According  
to customer requirement

**脚位定义Pin Definition****特征 Features**

- 低导通电阻Low Rds(on)@VGS= 10V
- 沟道功率MOS管TrenchFET Power MOSFET
- 无卤、RoHS认证Halogen-free、RoHS Compliant
- 表贴型封装Surface Mount Package

**应用 Applications**

- 直流/直流转换DC/DC Converter
- 电池驱动的便携式设备的电源管理Power Management in battery-driven Portables
- 开关电路Switching Circuits
- 高速线路驱动High-speed Line Driver

**等效电路 Equivalent circuit**

极限值和温度特性(TA = 25°C 除非另有规定)

**Maximum Ratings & Thermal Characteristics** (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
漏源电压Drain-Source Voltage	V <sub>DS</sub>	30	V
栅源电压Gate-Source Voltage	V <sub>GS</sub>	±20	V
漏极连续电流Continuous Drain Current	I <sub>D</sub>	5	A
漏极脉冲电流Pulsed Drain Current (note 1)	I <sub>DM</sub>	20	A
最大功耗Maximum Power Dissipation	P <sub>D</sub>	1.5	W
结环热阻Thermal Resistance from Junction to Ambient (note 2)	R <sub>θJA</sub>	100	°C/W
结温和存储温度Junction and Storage Temperature	T <sub>J</sub> , T <sub>STG</sub>	-50~+150	°C

电特性 (TA = 25°C 除非另有规定)

**Electrical Characteristics** (Ratings at 25°C ambient temperature unless otherwise specified).

参数 Parameters	符号 Symbol	测试条件 Test Condition	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
<b>静态特性Static Characteristics</b>						
漏源击穿电压 Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	30	--	--	V
零栅压漏极电流 Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	--	--	1	μA
栅源漏电流Gate-body leakage current	I <sub>GSS</sub>	V <sub>GS</sub> = ± 20V, V <sub>DS</sub> = 0V	--	--	±100	nA
栅源阈值电压 Gate threshold voltage (note 3)	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.0	1.6	2.5	V
漏源极导通电阻 Drain-source on-resistance (note 3)	R <sub>D(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 5A	--	24	30	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 3A	--	35	45	mΩ
二极管正向电压 Diode forward voltage (note 3)	V <sub>SD</sub>	I <sub>S</sub> = 4A, V <sub>GS</sub> = 0V	--	0.85	1.2	V

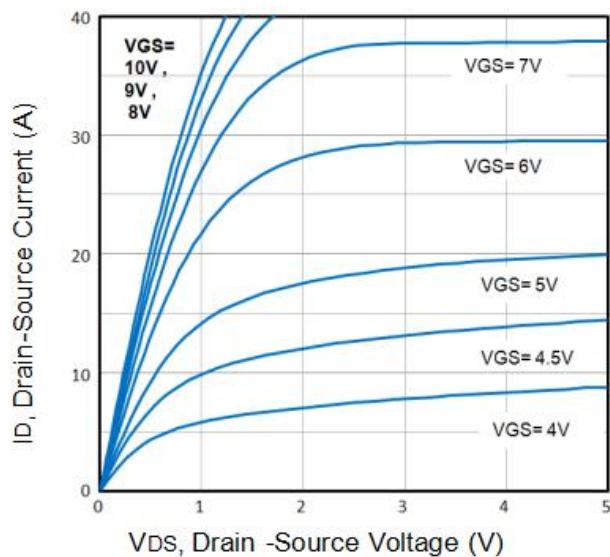
动态特性Dynamic Characteristics (note4)						
输入电容Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 15V, V <sub>GS</sub> =0V, f=1MHz	--	357	--	pF
输出电容Output Capacitance	C <sub>oss</sub>		--	52	--	pF
反向传输电容 Reverse Transfer Capacitance	C <sub>rss</sub>		--	45	--	pF
开关特性Switching Characteristics (note 4)						
开启延迟时间Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> = 15V, I <sub>D</sub> = 5A, R <sub>G</sub> = 3.3Ω, V <sub>GS</sub> = 10V	--	7	--	ns
开启上升沿时间Turn-on rise time	t <sub>r</sub>		--	18	--	ns
关断延迟时间Turn-off delay time	t <sub>d(off)</sub>		--	25	--	ns
关断下降沿时间Turn-off fall time	t <sub>f</sub>		--	11	--	ns
总栅极电荷Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 15V, I <sub>D</sub> =5A, V <sub>GS</sub> =10V	--	6.7	--	nC
栅源电荷Gate-Source Charge	Q <sub>gs</sub>		--	3.4	--	nC
栅漏电荷Gate-Drain Charge	Q <sub>gd</sub>		--	1.5	--	nC

**\*Notes :**

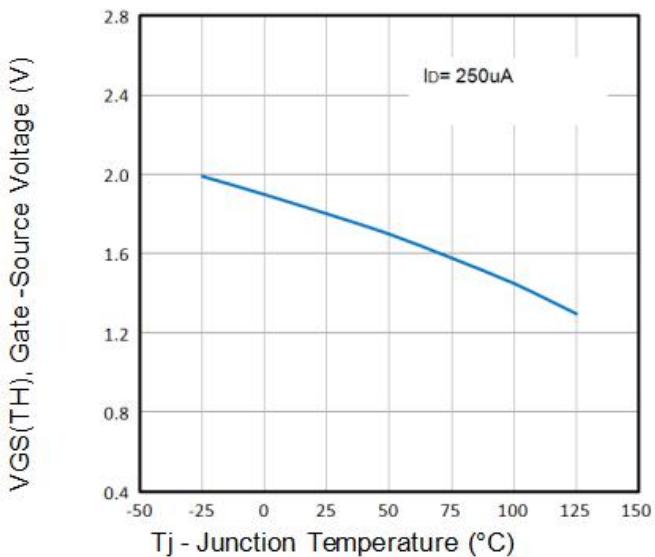
1. Repetitive rating: Pulse width limited by maximum junction temperature
2. Surface Mounted on FR4 board, t≤10 sec.
3. Pulse test : Pulse width≤300μs, duty cycle≤2%.
4. Guaranteed by design, not subject to production.



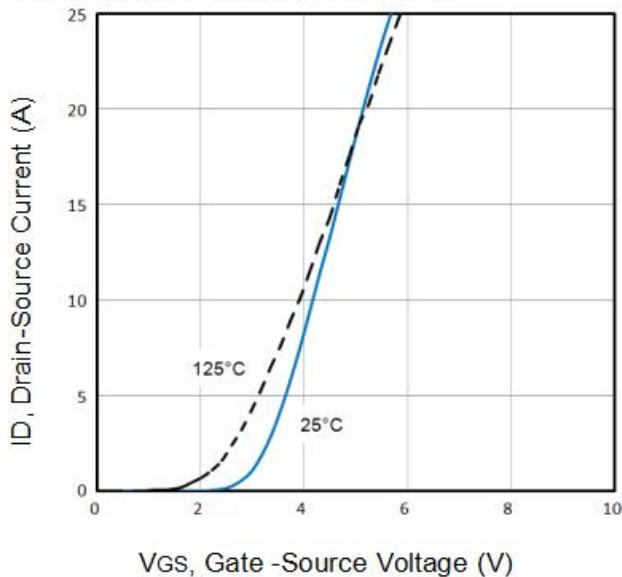
## 典型特性曲线 Typical characteristics



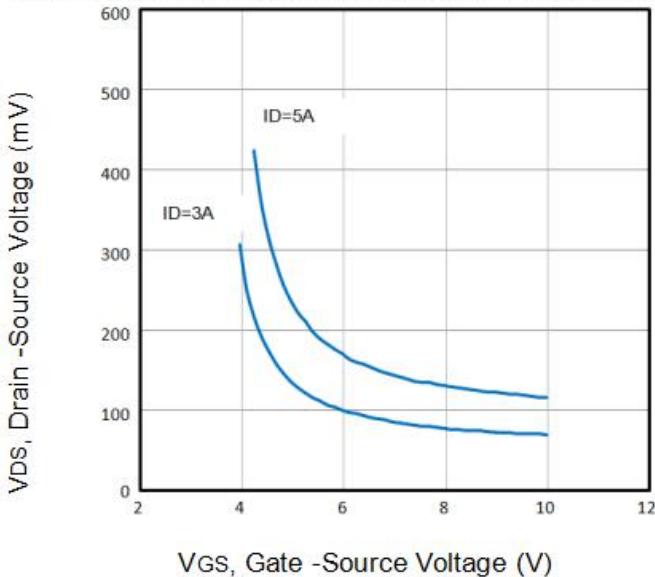
**Fig1.** Typical Output Characteristics



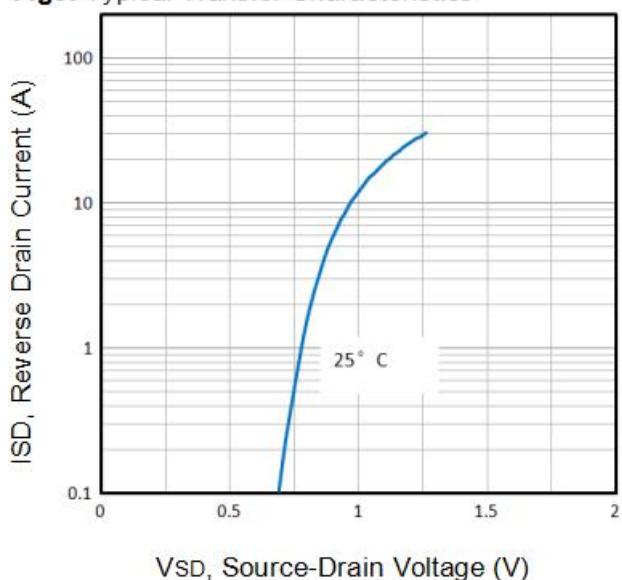
**Fig2.** Normalized Threshold Voltage Vs. Temperature



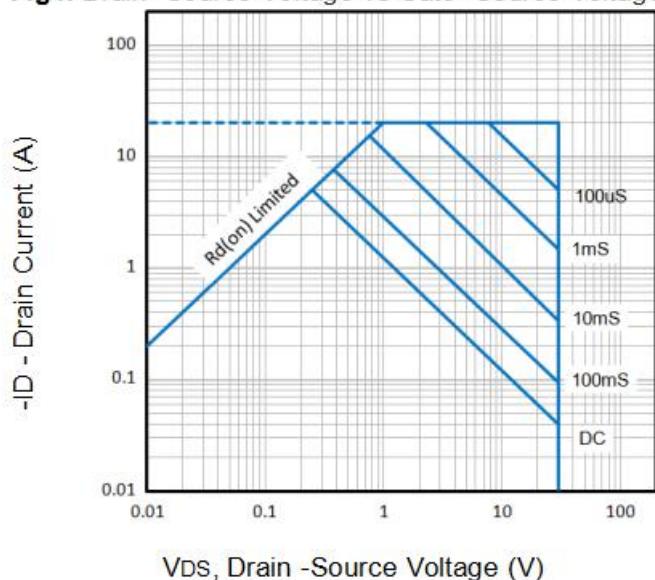
**Fig3.** Typical Transfer Characteristics



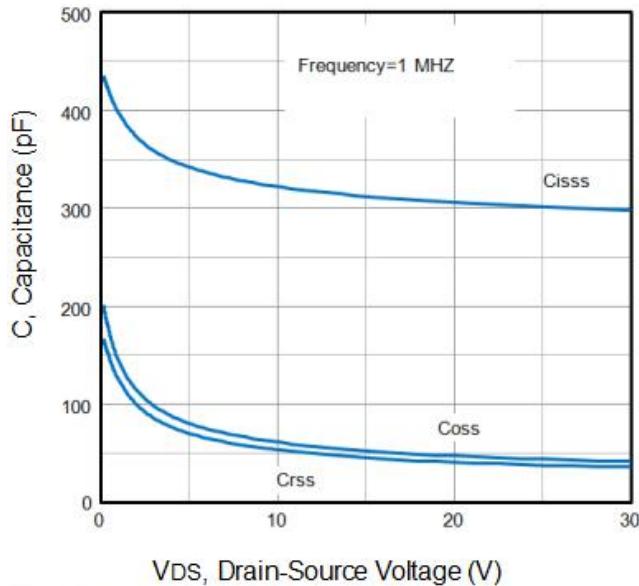
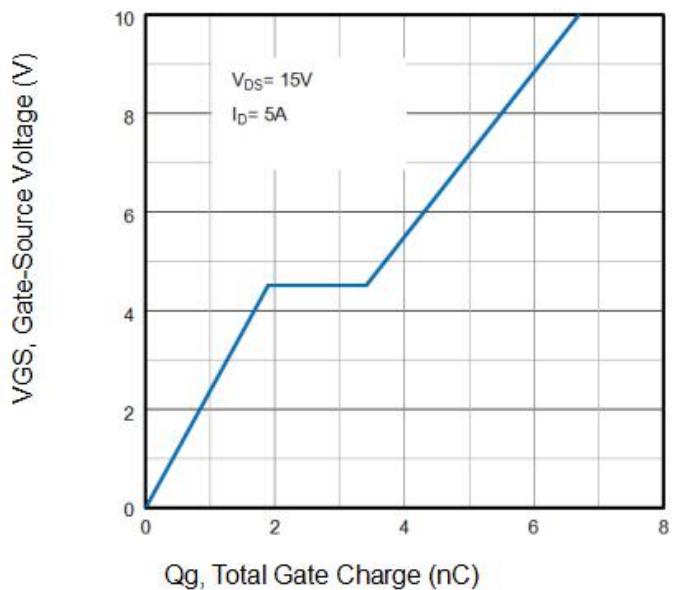
**Fig4.** Drain -Source Voltage vs Gate -Source Voltage



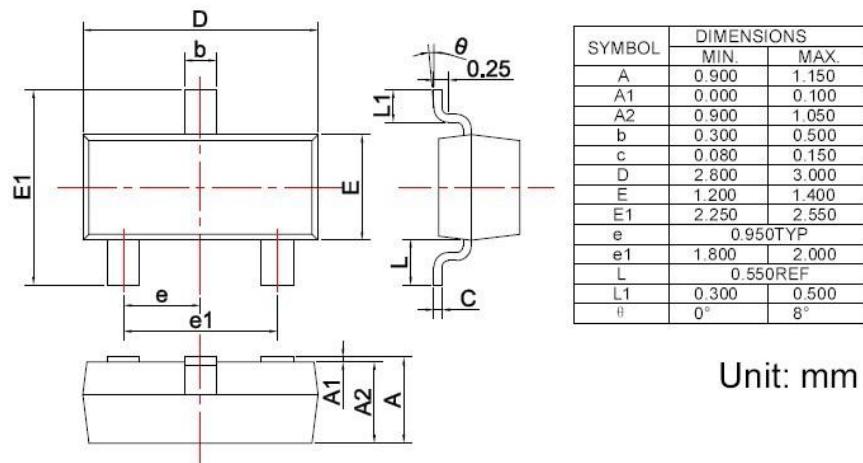
**Fig5.** Typical Source-Drain Diode Forward Voltage



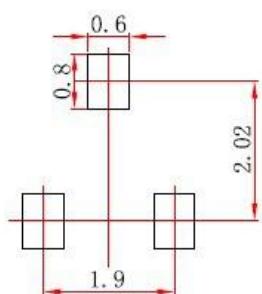
**Fig6.** Maximum Safe Operating Area

V<sub>DS</sub>, Drain-Source Voltage (V)**Fig7.** Typical Capacitance Vs. Drain-Source VoltageQ<sub>g</sub>, Total Gate Charge (nC)**Fig8.** Typical Gate Charge Vs. Gate-Source Voltage

### 封装外形图 SOT-23 Package Outline Dimensions



### 焊盘设计参考Precautions: PCB Design



#### Note:

- Controlling dimension: in millimeters.
- General tolerance: ± 0.05mm.
- The pad layout is for reference purposes only.