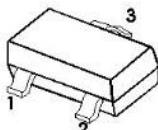


SOT-23N沟道20V漏-源电压MOS管
N-Channel 20V(D-S) Mosfet

产品特性总结Product Summary	
VDS	20V
RDS(on)(@VGS= 4.5V)	<35mΩ
RDS(on)(@VGS= 2.5V)	<45mΩ

根据客户要求打印 According
to customer requirement

脚位定义Pin Definition

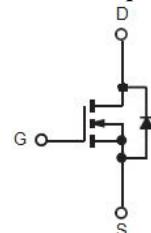
1. Gate
2. Source
3. Drain

特征 Features

- 低导通电阻Low Rds(on)@VGS= 4.5V
- 沟道功率MOS管TrenchFET Power MOSFET
- 高电流处理能力High Current Handling Capability
- 无卤、RoHS认证Halogen-free、RoHS Compliant
- 表贴型封装Surface Mount Package

应用 Applications

- 便携式设备的直流/直流转换DC/DC Converter for Portable Devices
- 开关电路Switching Circuits
- LED驱动LED Driver

等效电路 Equivalent circuit

极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameters	符号 Symbol	数值 Value	单位 Unit
漏源电压Drain-Source Voltage	VDS	20	V
栅源电压Gate-Source Voltage	VGS	±10	V
漏极连续电流Continuous Drain Current	ID	4.5	A
漏极脉冲电流Pulsed Drain Current (note 1)	IDM	18	A
最大功耗Maximum Power Dissipation	PD	1.2	W
结环热阻Thermal Resistance from Junction to Ambient (note 2)	R _{θJA}	100	°C/W
结温和存储温度Junction and Storage Temperature	T _J , T _{STG}	-50~+150	°C

电特性 (TA = 25°C 除非另有规定)

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

参数 Parameters	符号 Symbol	测试条件 Test Condition	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
静态特性Static Characteristics						
漏源击穿电压 Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, ID = 250μA	20	--	--	V
零栅压漏极电流 Zero gate voltage drain current	Idss	V _{DS} = 20V, V _{GS} = 0V	--	--	1	μA
栅源漏电流Gate-body leakage current	Igss	V _{GS} = ± 10V, V _{DS} = 0V	--	--	±100	nA
栅源阈值电压 Gate threshold voltage (note 3)	V _{GS(th)}	V _{DS} = V _{GS} , ID = 250μA	0.4	0.6	1.0	V
漏源极导通电阻 Drain-source on-resistance (note 3)	R _{D(on)}	V _{GS} = 4.5V, ID = 4A	--	28	35	mΩ
		V _{GS} = 3.3V, ID = 2A	--	32	40	mΩ
		V _{GS} = 2.5V, ID = 1A	--	36	45	mΩ
二极管正向电压 Diode forward voltage (note 3)	V _{SD}	I _S = 2A, V _{GS} = 0V	--	0.74	1.2	V



动态特性Dynamic Characteristics (note4)						
输入电容Input Capacitance	C _{iss}	V _{DS} = 10V, V _{GS} =0V, f=1MHz	--	280	--	pF
输出电容Output Capacitance	C _{oss}		--	46	--	pF
反向传输电容 Reverse Transfer Capacitance	C _{rss}		--	42	--	pF
开关特性Switching Characteristics (note 4)						
开启延迟时间Turn-on delay time	t _{d(on)}	V _{DD} = 10V, I _D = 4A, R _G = 3.3Ω, V _{GS} = 4.5V	--	11	--	ns
开启上升沿时间Turn-on rise time	t _r		--	35	--	ns
关断延迟时间Turn-off delay time	t _{d(off)}		--	25	--	ns
关断下降沿时间Turn-off fall time	t _f		--	32	--	ns
总栅极电荷Total Gate Charge	Q _g	V _{DS} = 10V, I _D =3A, V _{GS} =5V	--	4.7	--	nC
栅源电荷Gate-Source Charge	Q _{gs}		--	0.6	--	nC
栅漏电荷Gate-Drain Charge	Q _{gd}		--	1.7	--	nC

***Notes :**

1. Repetitive rating: Pulse width limited by maximum junction temperature
2. Surface Mounted on FR4 board, t≤10 sec.
3. Pulse test : Pulse width≤300μs, duty cycle≤2%.
4. Guaranteed by design, not subject to production.



典型特性曲线 Typical characteristics

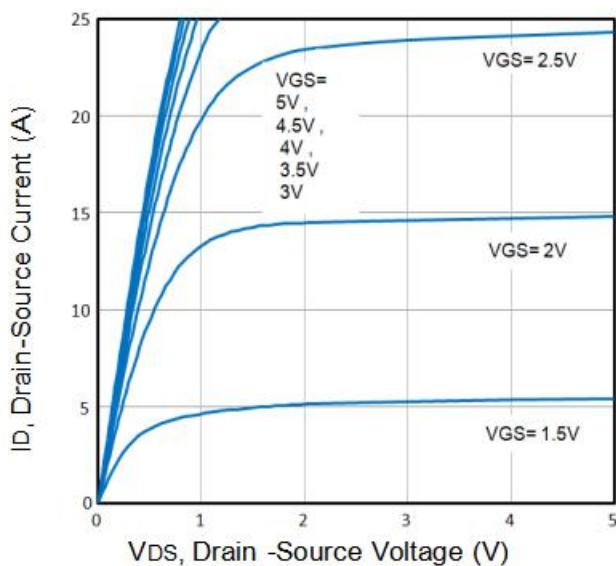


Fig1. Typical Output Characteristics

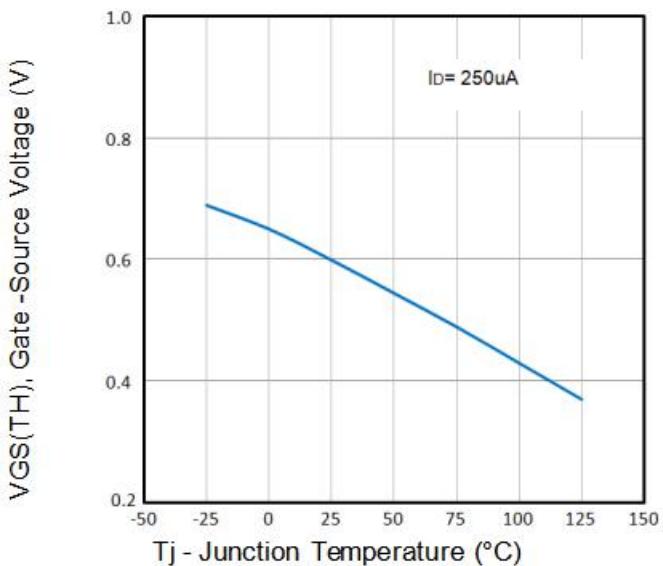


Fig2. Normalized Threshold Voltage Vs. Temperature

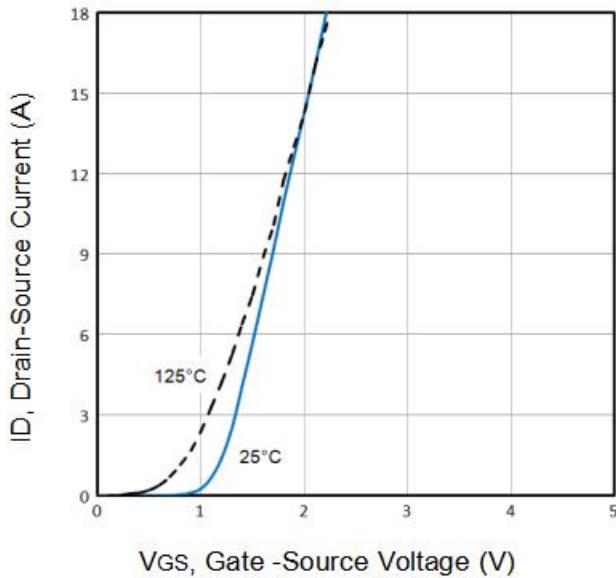


Fig3. Typical Transfer Characteristics

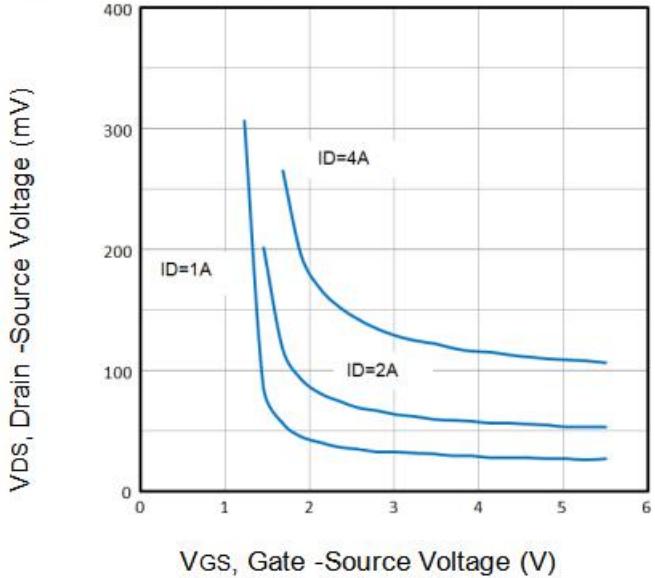


Fig4. Drain -Source Voltage vs Gate -Source Voltage

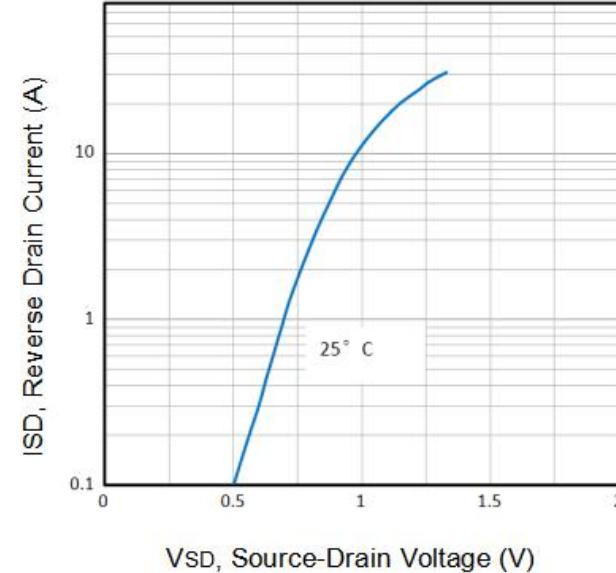


Fig5. Typical Source-Drain Diode Forward Voltage

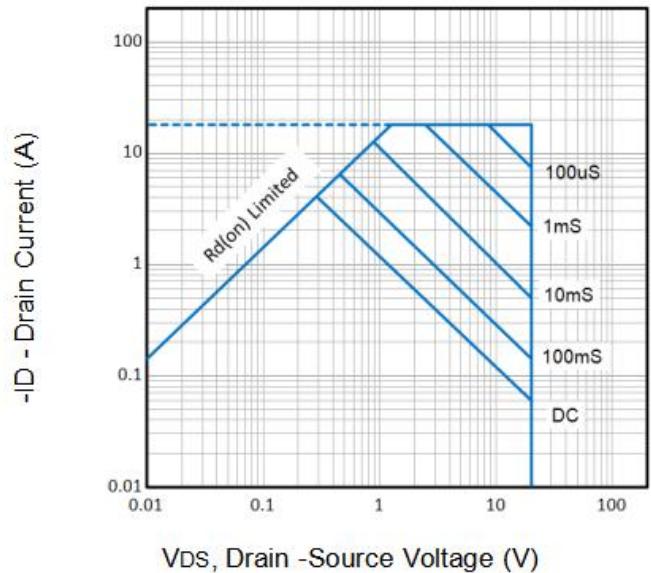
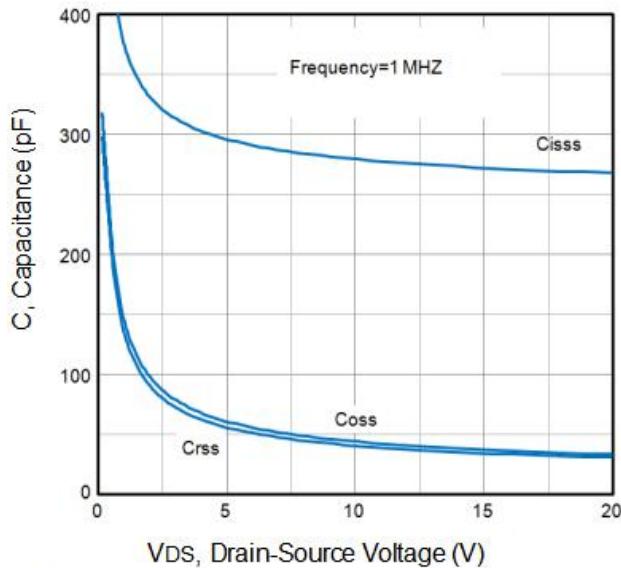
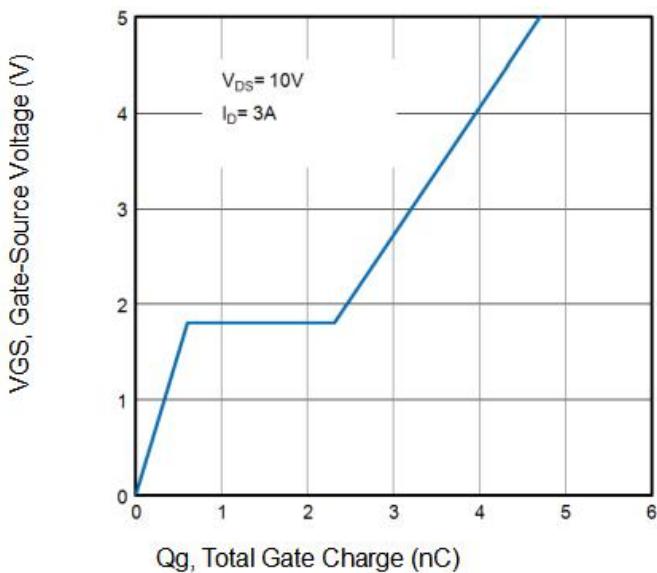
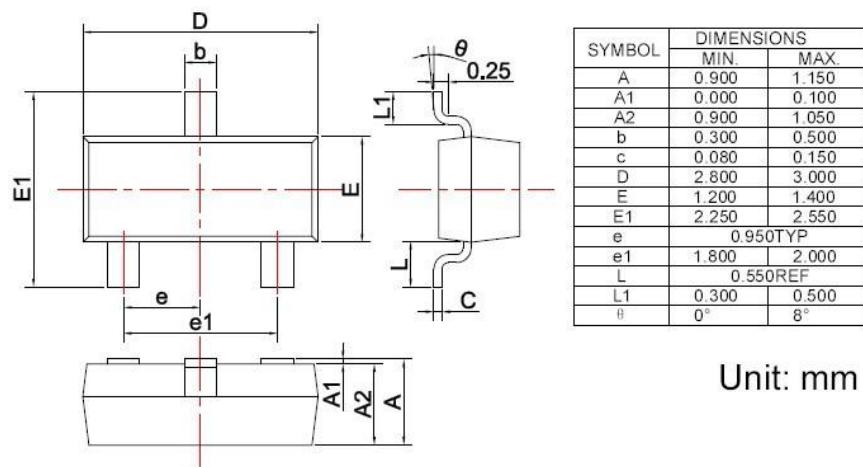


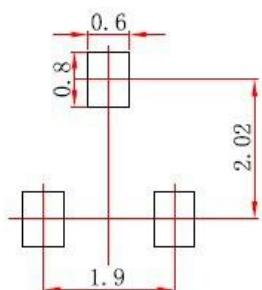
Fig6. Maximum Safe Operating Area

**Fig7.** Typical Capacitance Vs. Drain-Source Voltage**Fig8.** Typical Gate Charge Vs. Gate-Source Voltage

封装外形图 SOT-23 Package Outline Dimensions



焊盘设计参考Precautions: PCB Design



Note:

1. Controlling dimension: In millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.