

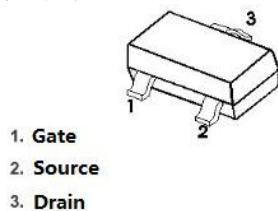
SOT-23

P沟道60V漏-源电压MOS管
P-Channel 60V(D-S) Mosfet

产品特性总结Product Summary	
VDS	-60V
RDSON(@VGS= -10V)	<200mΩ
RDSON(@VGS= -4.5V)	<300mΩ

根据客户要求打印 According to customer requirement

脚位定义Pin Definition



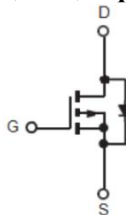
特征 Features

- 低导通电阻Low Rds(on)@VGS= -10V
- 沟道功率MOS管TrenchFET Power MOSFET
- 无卤、RoHS认证Halogen-free、RoHS Compliant
- 表贴型封装Surface Mount Package

应用 Applications

- 用于便携式设备的直流/直流转换DC/DC Converter for Portable Devices
- 用于便携式设备的负载开关Load Switch for Portable Devices
- 高速线路驱动High-speed Line Driver
- 开关电路Switching Circuits

等效电路Equivalent circuit



极限值和温度特性(TA = 25°C 除非另有规定)

Maximum Ratings & Thermal Characteristics (Ratings at 25°C ambient temperature unless otherwise specified.)

参数 Parameters	符号Symbol	数值 Value	单位Unit
漏源电压Drain-Source Voltage	V _{DS}	-60	V
栅源电压Gate-Source Voltage	V _{GS}	±20	V
漏极连续电流Continuous Drain Current	I _D	-2	A
漏极脉冲电流Pulsed Drain Current (note 1)	I _{DM}	-8	A
最大功耗Maximum Power Dissipation	P _D	1.0	W
结环热阻Thermal Resistance from Junction to Ambient (note 2)	R _{θJA}	125	°C/W
结温和存储温度Junction and Storage Temperature	T _J , T _{STG}	-50~+150	°C

电特性 (TA = 25°C 除非另有规定)

Electrical Characteristics (Ratings at 25°C ambient temperature unless otherwise specified).

参数 Parameters	符号 Symbol	测试条件 Test Condition	最小值 Min	典型值 Typ	最大值 Max	单位 Unit
静态特性Static Characteristics						
漏源击穿电压 Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = -250μA	-60	--	--	V
零栅压漏极电流 Zero gate voltage drain current	I _{DSS}	V _{DS} = -60V, V _{GS} = 0V	--	--	-1	μA
栅源漏电流Gate-body leakage current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	--	--	±100	nA
栅源阈值电压 Gate threshold voltage (note 3)	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-1.5	-2.5	V
漏源极导通电阻 Drain-source on-resistance (note 3)	R _{DS(on)}	V _{GS} = -10V, I _D = -2A	--	150	200	mΩ
		V _{GS} = -4.5V, I _D = -1A	--	200	300	mΩ
二极管正向电压 Diode forward voltage (note 3)	V _{SD}	I _S = -2A, V _{GS} = 0V	--	-0.84	-1.2	V

动态特性Dynamic Characteristics (note4)						
输入电容Input Capacitance	Ciss	VDS= -30V,VGS=0V, f=1MHz	--	310	--	pF
输出电容Output Capacitance	Coss		--	22	--	pF
反向传输电容 Reverse Transfer Capacitance	Crss		--	15	--	pF
开关特性Switching Characteristics (note 4)						
开启延迟时间Turn-on delay time	td(on)	VDD= -30V,Id= -2A,RG= 3.3Ω, VGS= -10V	--	41	--	ns
开启上升沿时间Turn-on rise time	tr		--	22	--	ns
关断延迟时间Turn-off delay time	td(off)		--	25	--	ns
关断下降沿时间Turn-off fall time	tf		--	32	--	ns
总栅极电荷Total Gate Charge	Qg	VDS= -30V,Id=-2A, VGS=-10V	--	5.4	--	nC
栅源电荷Gate-Source Charge	Qgs		--	1.1	--	nC
栅漏电荷Gate-Drain Charge	Qgd		--	1.6	--	nC

***Notes :**

1. Repetitive rating: Pluse width limited by maximum junction temperature
2. Surface Mounted on FR4 board, $t \leq 10$ sec.
3. Pulse test : Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production.

典型特性曲线 Typical characteristics

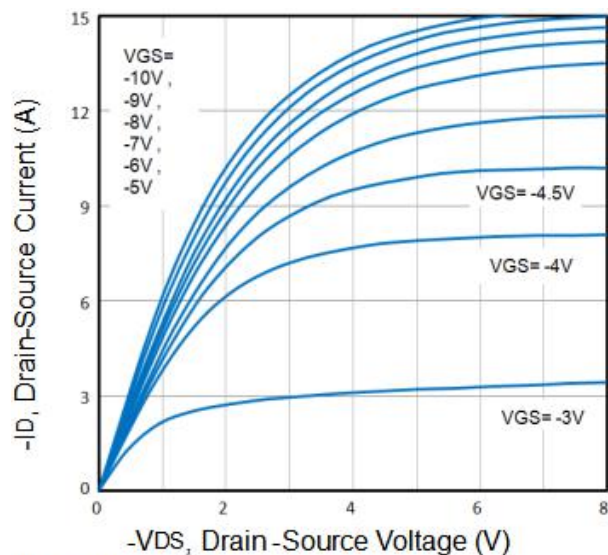


Fig1. Typical Output Characteristics

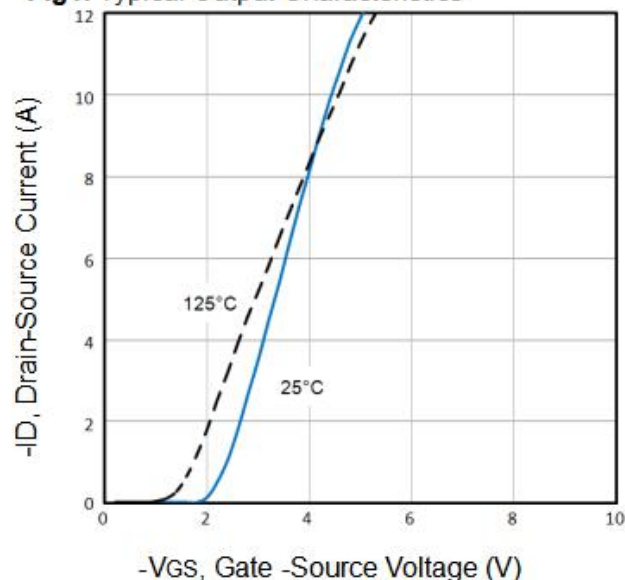


Fig3. Typical Transfer Characteristics

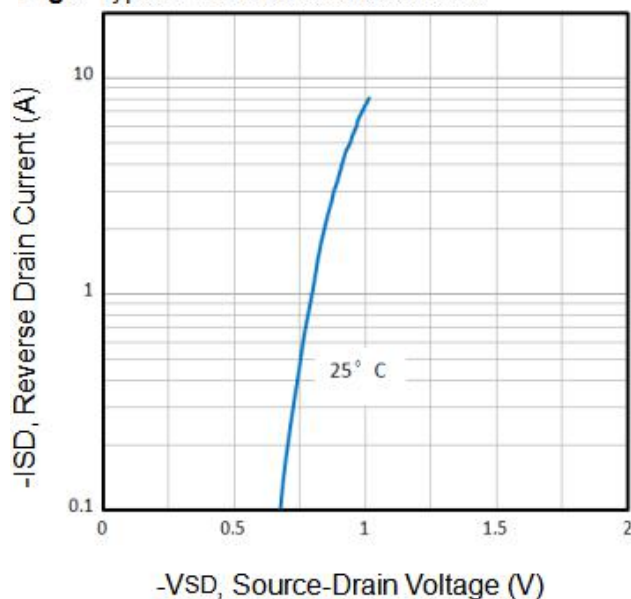


Fig5. Typical Source-Drain Diode Forward Voltage

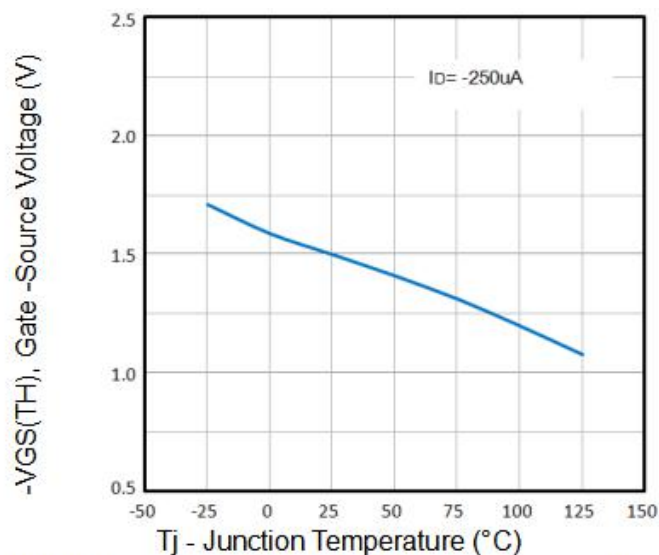


Fig2. Normalized Threshold Voltage Vs. Temperature

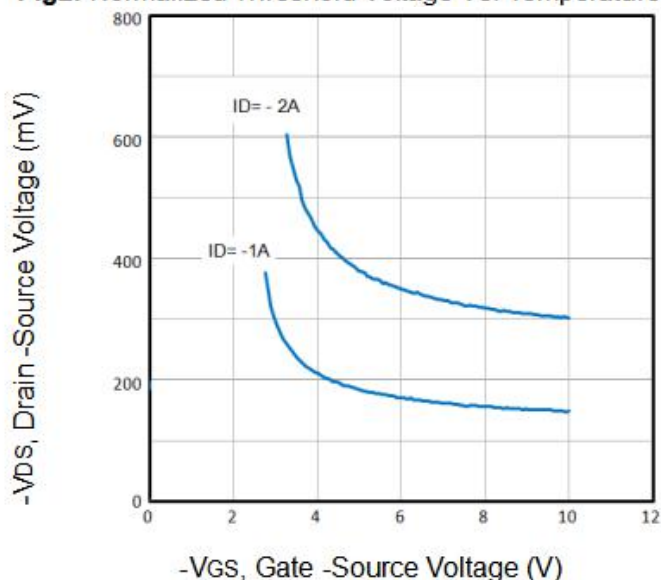


Fig4. Drain-Source Voltage vs Gate-Source Voltage

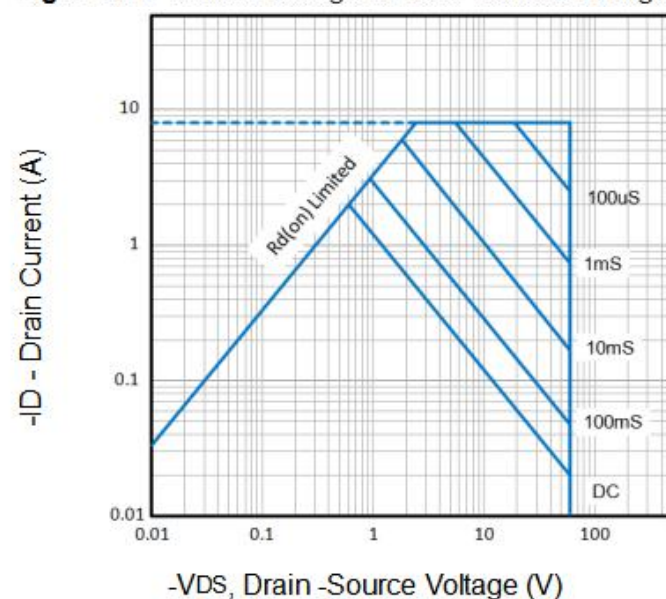


Fig6. Maximum Safe Operating Area

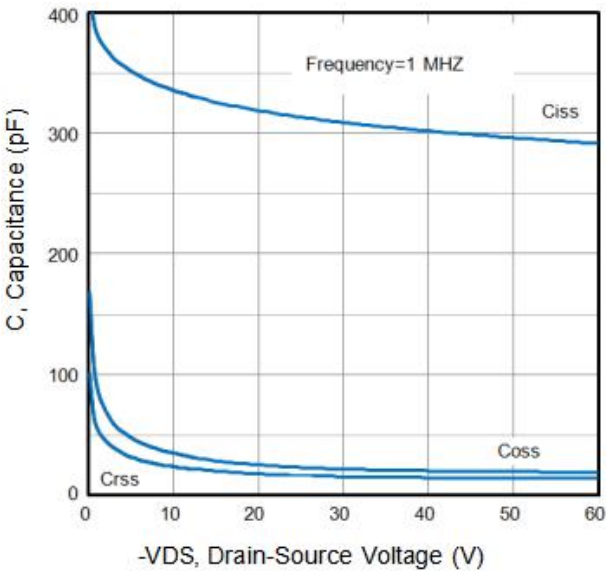


Fig7. Typical Capacitance Vs. Drain-Source Voltage

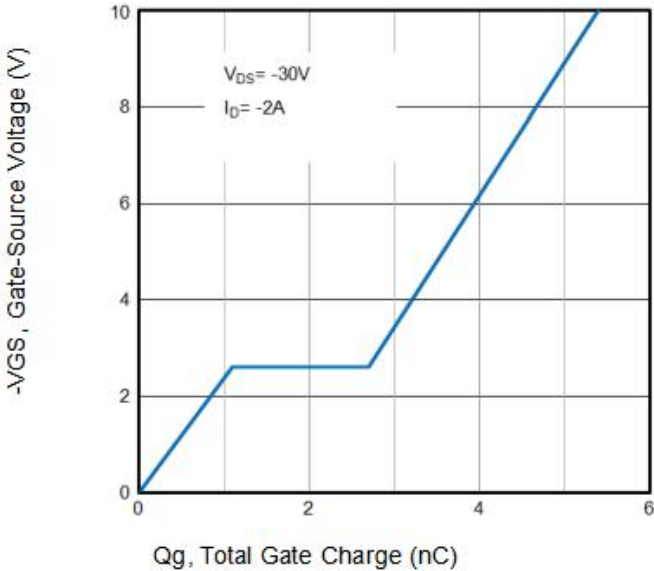
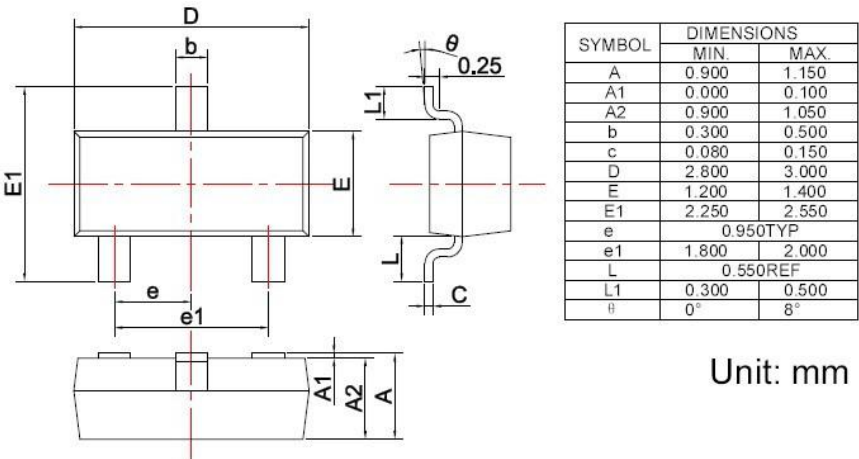
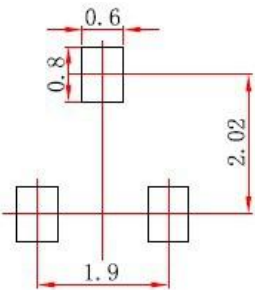


Fig8. Typical Gate Charge Vs. Gate-Source Voltage

封装外形图 SOT-23 Package Outline Dimensions



焊盘设计参考Precautions: PCB Design



Note:
1. Controlling dimension: in millimeters.
2. General tolerance: ±0.05mm.
3. The pad layout is for reference purposes only.